PATENT NUMBER U.S. UTILITY Patent Application O.I.P.E. PATENT DATE SCANNED Elec 3 APPLICATION NO. CONT/PRIOR CLASS 126 SUBCLASS ART UNIT **EXAMINER** 09/823058 2804 Tabsuya Suguit 1763 Strok/transfer vegsel for semiconductor substrate and method of manufacturing semiconductor device PTO-2040 12/99 SCHNES ASSIRATION William Street Street Street - TERMINAL \*\* DHAWINGS DISCLAIMER Sheets Drwg. Figs. Drwg. Print Fig. The term of this patent NOTICE OF ALLOWANCE MAILED KAPLA MOOPE 4/4/60 subsequent to has been disclaimed. The term of this patent shall not extend beyond the expiration date of U.S Patent. No. ISSUE FEE Amount Due Date Paid ISSUE BATCH NUMBER The terminal \_\_ months of this patent have been disclaimed." WARNING: formation disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 366 selon outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only. FILED WITH: DISK (CRF) FICHE CO-ROW